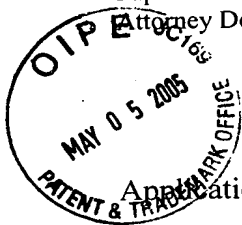


Application No. 10/701,841  
Paper Dated: May 2, 2005  
Attorney Docket No.: 1880-031249



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/701,841 Confirmation No.: 7054  
Applicant : Robert J. Hillard  
Filed : November 5, 2003  
Title : METHOD OF ELECTRICAL CHARACTERIZATION  
OF A SILICON-ON-INSULATOR (SOI) WAFER  
Art Unit : 2818  
Examiner : Dung Anh Le  
Customer No. : 28289

**LETTER TO OFFICIAL DRAFTSPERSON**

Commissioner for Patents  
Mail Stop Issue Fee  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is one (1) set of formal drawings comprised of six (6) sheets including Figs. 1-6. Please substitute the enclosed formal drawings for the drawings currently on file in the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 2, 2005.

Deborah L. Hartmann  
(Name of Person Mailing Paper)

Deborah L. Hartmann 05/02/05  
Signature Date

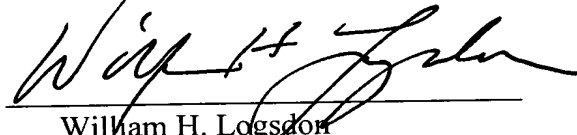
Application No. 10/701,841  
Paper Dated: May 2, 2005  
Attorney Docket No.: 1880-031249

No new matter has been added in the enclosed drawings.  
Entry of the enclosed drawings is respectfully requested.

Respectfully submitted,

THE WEBB LAW FIRM

By



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